



ROBERT FULGAR BARNEDO

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CAREER SUMMARY

An Electronic Engineer with 22 years of experience in a Research and Development which mainly focuses on development of electronic circuit designs. Started a career in an R and D environment as electronic engineer which focus on the design of machine controls, from design ideas, simulation, prototyping and applications. Engaged in a semiconductor company which supports on evaluation of IC. After which, have been mostly involve in a failure analysis of Optical Transceivers. Held position in Quality Assurance department focusing on customer return for analysis and is currently holding the position of Failure Analysis Engineer in Finisar.

PROFESSIONAL EXPERIENCE

FINISAR SDN BHD MALAYSIA (June 2008 – Present)

Chemor, Perak Malaysia

Failure Analysis Engineer

Performs failure analysis of the Optical Transceiver products.

- Works with Product Engineering to better address customer concerns on quality of the Optical Transceiver Products delivered to the customer.
- Perform hands-on analysis and debugging of customer's reported problem and ensure a quick turn-around-time for each returned product.
- Assess Return Material Authorization (RMA), Identify the root cause of the failure, corrective and preventive actions. Coordinate the results of the failure analysis to the internal department and provide FA reports to the customer.
- Maintains knowledge of products capabilities and characteristics.

ROHM LSI DESIGN PHILIPPINES, INC. (July 2005 – June 2008)

Makati City, Philippines

R & D Engineer

- Integrated Circuit Tracing
 - Performs manual tracing of the transistor circuitry of an integrated circuits.
 - Performs simple analysis in order to redraw the traced circuits to a functional schematics.
 - Performs schematics layout of the traced circuits using Schematic Composer.
 - Preparation of documents regarding the traced schematics and details of the IC.
- IC Evaluation
 - Evaluate design samples (sample IC) in both normal and worst case conditions to acquire its electrical characteristics.
 - Create program using Agilent VEE to automate laboratory instruments for faster measurements and data acquisitions.
 - Preparation of reports on the evaluation of the sample IC.

Northwest Construction and Tech Inc (May 1994 – February 2005)

Quezon City, Philippines

ELECTRONIC LEAD ENGINEER

Provide technical supports on the implementation of the electronics designs.

- Responsible in simulation and prototyping of electronic circuits design (power electronics) for AC and DC drives as well as electronic controls.
- Work closely with the mechanical engineers regarding controls for the design of various machines.
- Prepare schematic design in documents and layout the corresponding PCB routing using CAD (Protel 99 SE).

QUALIFICATIONS, SKILLS AND TECHNICAL BACKGROUND

- Knowledgeable in the following design tools: Cadence Schematic Composer, Cadence Analog Artist (SpectreS), SECS (Synario Capture Systems), PROTEL 99 SE, PSPICE, AGILENT VEE.
- Knowledgeable in the following languages: C
- Familiar with the following computer applications: Open Office, different Microsoft™ Development Tools (POWERPOINT, EXCEL, WORD)
- Capable of using laboratory instruments such as Digital Multi-meter, Power Supply, Oscilloscope, Optical scope (DCA), optical switches, Current/Voltage Source, Function/Arbitrary Waveform Generator, Bit Error Rate analyzer.
- Knowledgeable in Circuit design and Trouble shooting.
- Knowledge in Power Electronics Principles.

EDUCATION

- 1997 – 2004 CENTRAL COLLEGES of the PHILIPPINES
Bachelor of Science Degree in Electronics and Communications Engineering,
October 2004
- 1991 – 1992 DON BOSCO TECHNICAL COLLEGE
Manpower Training Skills (Radio Mechanics, TV-Video)
June 1992

PERSONAL BACKGROUND

Born on May 23, 1972 in Mandaluyong, Philippines. Fluent in English and Filipino and a bit in Bahasa Malaysia. Interests include playing badminton, watching movies, surfing the net, circuit design and troubleshooting. Responsible, honest, team player, dependable and goal-oriented.

REFERENCES

References are available upon requests.